

REGENT'S

FlipFlop™

BI-STABLE RELAY

FlipFlop120 for 120 VAC Systems

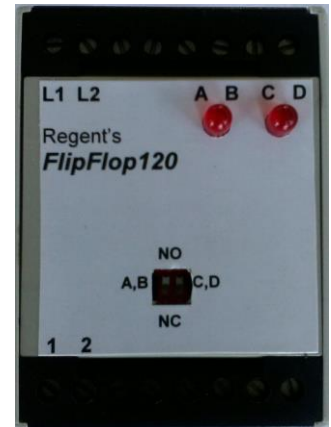
FlipFlop32 for DC Systems

Features

- Bi-stable or latching function to turn load on with momentary or maintained input signal.
- Two output poles, independently convertible from normally-open to normally-closed.
- Complete isolation between line, load, and logic terminals.
- Compact size. DIN rail or panel mount.
- LED status indicator for each output pole.
- Regent's 2 Year Warranty.

Ideal for:

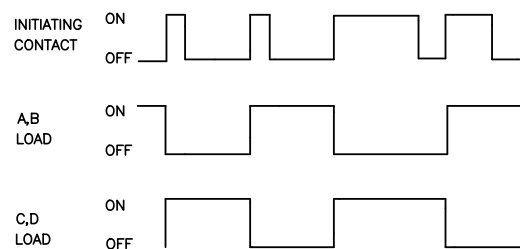
- ▶ Alternating product flow on packaging lines between two conveyors or packers
- ▶ Skipping every other cycle on a printing or gluing operation
- ▶ Alternately diverting containers on a labeling or sealing line
- ▶ Duplex pump control
- ▶ Alternating between two timing cycles for filling or gluing
- ▶ Alternating tool feed
- ▶ Forward/Reverse cycle control



The *FlipFlop* is an all-solid-state relay which provides the flip-flop, or toggle function. The relay can provide alternate energization of solenoids, pneumatic valves, motor starters, solid-state control devices, and other industrial loads. The *FlipFlop* is a bi-stable relay, and is designed to replace electromechanical ratchet relays for improved reliability and speed.

The *FlipFlop* is pulse triggered, so control voltage may be momentary or maintained. Input can be provided by mechanical limit switches, proximity switches, photo-electric controls, programmable controller output modules or other solid-state devices. Relays can be cascaded to implement an n-stage binary shift register.

TIMING DIAGRAM



FOR MORE INFORMATION CALL 203-732-6200
OR VISIT US ONLINE AT www.regentcontrols.com

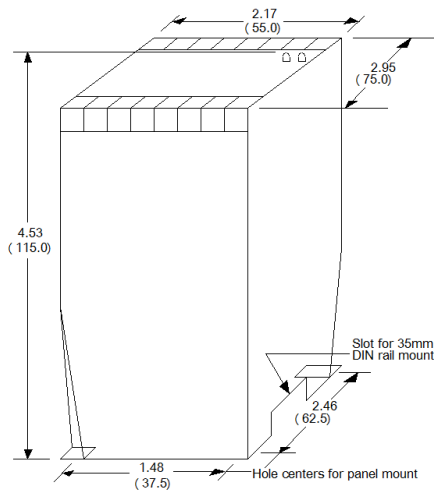


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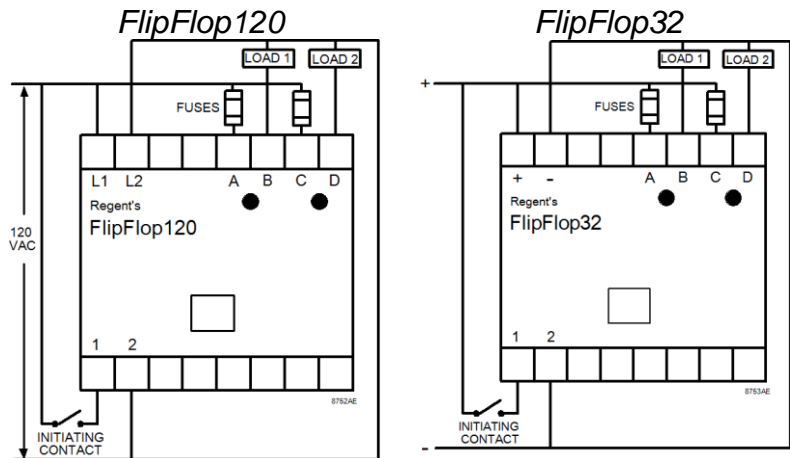


Regent's *FlipFlop* Bi-Stable Relay

DIMENSIONS



WIRING DIAGRAMS



NOTES

1. There is complete electrical isolation among Line, Load, and Logic circuits. They may be used in separate and different voltage circuits or systems.
2. Terminals L2 (or -) and/or 2 may be grounded.
3. For loads greater than 1 amp, do not parallel solid-state switches. The current will not divide equally and may result in damage.
4. Normally-closed switches require power on L1,L2 (or +,-) terminals for proper operation.
5. Switching time delay is the delay between A,B turn-off and C,D turn-on (or vice versa) to prevent overlap.

| SPECIFICATIONS | <i>FlipFlop120</i> | <i>FlipFlop32</i> |
|---|---|---|
| Line Input (L1,L2 or +,- terminals) | 120 VAC +/- 20%, 50/60 Hz 15 mA burden | 12-30 VDC, 5% ripple max, 35 mA burden |
| Logic Input (1,2 terminals) | 120 VAC +/- 20%, 50/60 Hz, 25 mA burden (will not operate on leakage current below 10 mA) | 3-32 VDC, 1-35 mA burden sinking or sourcing |
| Logic Response Time (excluding Switching Time Delay, see note 5) | 0.3 to 4 msec | <1 msec |
| Load Switch (A,B and C,D terminals) Rating | 120 VAC +/- 20%, 1 A continuous 5A inrush; resistive or inductive less than 2 mA at 65°C | 3-32 VDC 1A max; resistive or inductive less than 100uA at 65°C |
| Off-state leakage On-state voltage drop Minimum load current Recommended load fuse | 1 VAC typical at rated current less than 15 mA Littelfuse 322002 | 1 VDC maximum at rated current less than 1 mA Buss PCB1 |
| Switching Time Delay (see note 5) | 12 msec typical | 10 msec typical |
| Temperature | 0 to 65°C (32 to 149°F) | 0 to 65°C (32 to 149°F) |

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